



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Kusumoto et al. Art Unit : 2814
Serial No. : 09/903,339 Examiner : Theresa T. Doan
Filed : July 10, 2001
Title : METHOD FOR PRODUCING INSULATED GATE THIN FILM
SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants call attention to the attached Information
Disclosure Statement and documents listed on form PTO-1449.

This filing is being made before the receipt of a first
Office action on the merits. No fee is required.

Each enclosed document listed on the herewith Form PTO-1449
was cited in a foreign counterpart application not more than
three months ago. To the best knowledge of the undersigned,
this is the first citation of these items in any communication
from a foreign patent office in a counterpart foreign
application.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

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September 9, 2002

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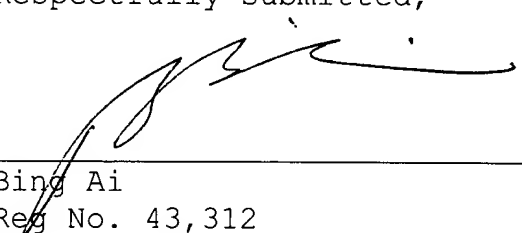



Consideration of the foregoing and enclosures plus the
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Examiner's initials in the left column per MPEP 609 are
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Respectfully submitted,

Date: September 9, 2002


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